

PATENT ABSTRACTS OF JAPAN

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(21)Application number : **08-246165** (71)Applicant : **CASIO COMPUT CO LTD**

(22)Date of filing : **12.10.1994** (72)Inventor : **ONAKA EIICHI**

(54) ACTIVE MATRIX PANEL AND ITS PRODUCTION

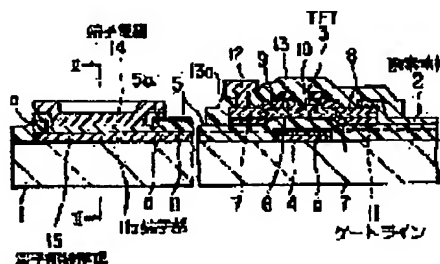
(57)Abstract:

PURPOSE: To prevent the occurrence of the conduction defect of gate line terminal parts and to improve the yield of production by providing gate lines with

coating films consisting of the same transparent conductive films as the transparent conductive films of pixel electrodes on their terminal parts and forming terminal electrodes thereon.

CONSTITUTION: A transparent substrate 1 is provided thereon with the plural pixel electrodes 2, plural thin-film transistors (TFTs) 3 respectively corresponding to the pixel electrodes 2 and the gate lines

11 and data lines 12 for supplying gate signals and data signals to the TFTs 3. Further, the substrate is provided with interlayer insulating films 13 which cover the TFTs 3 and correspond to the wiring regions of the data lines 12 and the data lines 12 are connected to the drain electrodes 9 of the TFTs 3 in their contact holes 13a. On the other hand, the terminal parts 1a of the gate lines 11 are exposed by openings of the gate insulating films 5. These terminal parts 11a are provided thereon with terminal part coating films 15 which consist of the same transparent conductive films as the transparent conductive films of the pixel electrodes 2 and cover the entire surface of the terminal parts



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11a. The terminal electrodes 14 connected to gate side driving circuits are formed thereon.

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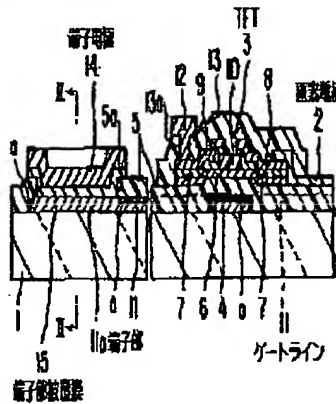
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(54) 【発明の名称】 アクティブマトリクスパネルおよびその駆動方法

(57) 【要約】

【目的】 駆動中にゲートラインの端子部分に短絡不良が発生するのを防いで駆動効率を向上させることができるアクティブマトリクスパネルを提供する。

【構成】 ゲートライン11の端子部11aの上に、西向き電圧と同じ透明導電膜からなる端子部被覆膜15を設け、その上に端子電極14を形成した。



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